

**Course: FPGA Based System Design**

**Semester: 8th**

**Class: ECT-2K9**

**Campus: Chakwal**

**Note: For any query contact me as soon as possible**

	Schematic	Counter	FSM	FPGA Arch.	Assign			
	Assign 01	Assign 02	Assign 03	Assign 04	Total (20)	Labs (20)	Mid-exam	Sessional
09-ECT-01	x	x	x	x			x	x
09-ECT-02	10	10	10	9	20	20	17	57
09-ECT-03	9	10	10	9	19	20	10	49
09-ECT-04	3	10	10	9	16	20	18	54
09-ECT-05	10	10	10	8	19	18	8	45
09-ECT-06	8	10	10	10	19	20	14	53
09-ECT-07	8	10	10	8	18	20	17	55
09-ECT-08	x	x	x	x	0	0	x	x
09-ECT-09	10	10	10	8	19	20	9	48
09-ECT-10		10	0		5	15	10	30
09-ECT-11	10	10	10	8	19	20	17	56
09-ECT-12	10	10	10	9	20	20	9	49
09-ECT-13	9	10	10	9	19	20	18	57
09-ECT-14		10	10	8	14	15	11	40
09-ECT-15	10	10	10	7	19	20	14	53
09-ECT-16	10	10	10	8	19	20	16	55
09-ECT-17	8	10	10	9	19	20	20	59
09-ECT-18	9	10	10	8	19	20	16	55
09-ECT-19	10	10	10	8	19	20	11	50
09-ECT-20	8	10	10	7	18	20	16	54
09-ECT-21		10	10	9	15	20	19	54
09-ECT-22		10	10	8	14	20	9	43
09-ECT-23	8	10	10	9	19	20	18	57

09-ECT-24	x	x	x	x	0		x	x
09-ECT-25		10	10	8	14	20	14	48
09-ECT-26	8	10	10		14	20	14	48
09-ECT-27	6	10	10	8	17	20	14	51
09-ECT-28	9	10	10	8	19	20	12	51
09-ECT-29	8	10	10	8	18	20	19	57
09-ECT-30	8	10	10	8	18	20	18	56
09-ECT-31	10	10	10	8	19	20	18	57
09-ECT-32	10	10	10	8	19	17	6	42
09-ECT-33	10	10	10	9	20	20	9	49
09-ECT-34	8	10	10	9	19	20	17	56
09-ECT-35	x	x	x		0		x	x
09-ECT-36	9	10		8	14	20	16	50
09-ECT-37	5	10	10	9	17	20	19	56
09-ECT-38	10	10	10	8	19	18	18	55
09-ECT-39	10	10	10	8	19	20	11	50
09-ECT-40	8	10	10	8	18	20	16	54
09-ECT-41	8	10	10	8	18	18	7	43
09-ECT-42		10	10	8	14	15	6	35
09-ECT-43	8	10	10	8	18	20	7	45
09-ECT-44	10	10	10	8	19	20	19	58
09-ECT-45	8	10	10	8	18	20	15	53
09-ECT-46	10	10	10	9	20	20	14	54
09-ECT-47	6	10	0	6	11	15	19	45
09-ECT-48	8	10	10	9	19	17	7	43
09-ECT-49	10	10	10	8	19	20	11	50
09-ECT-50	9	10	10	8	19	18	12	49
09-ECT-51		10	10	6	13	13	6	32
09-ECT-52	10	10	10	8	19	20	17	56